

Fig. 1

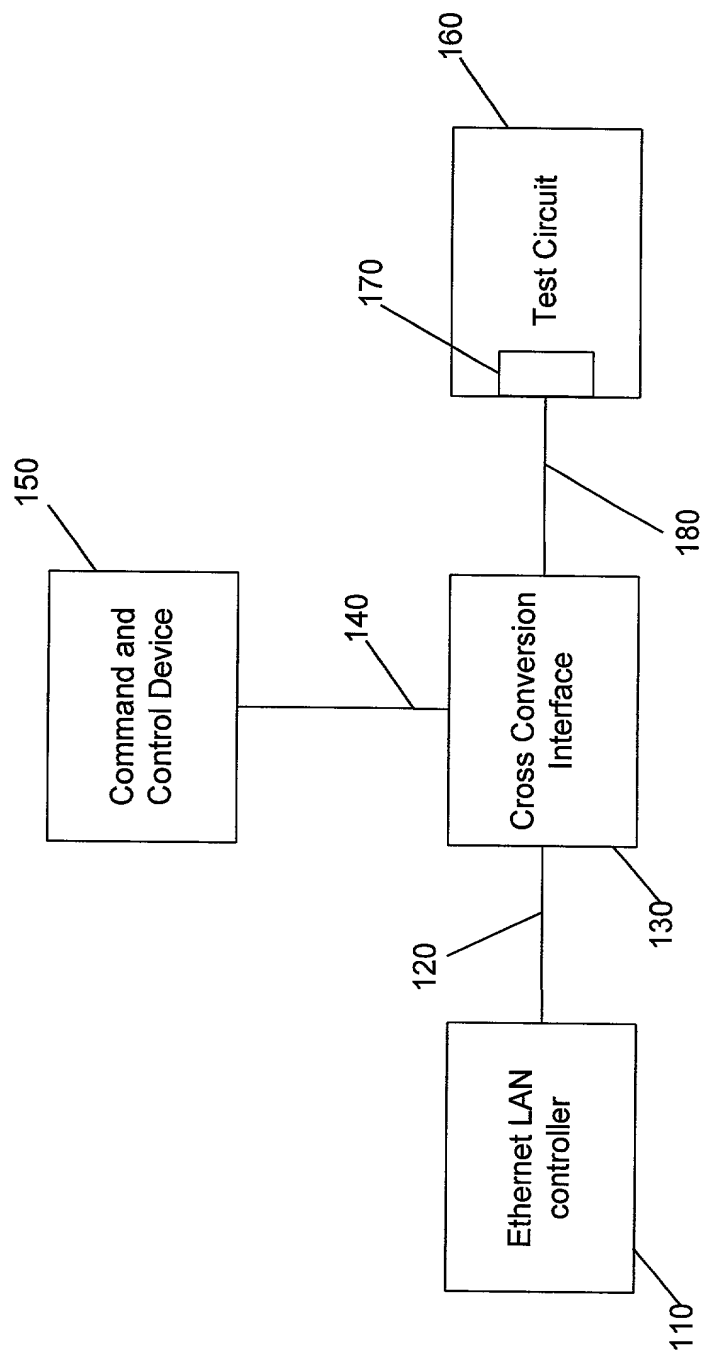


FIG. 1

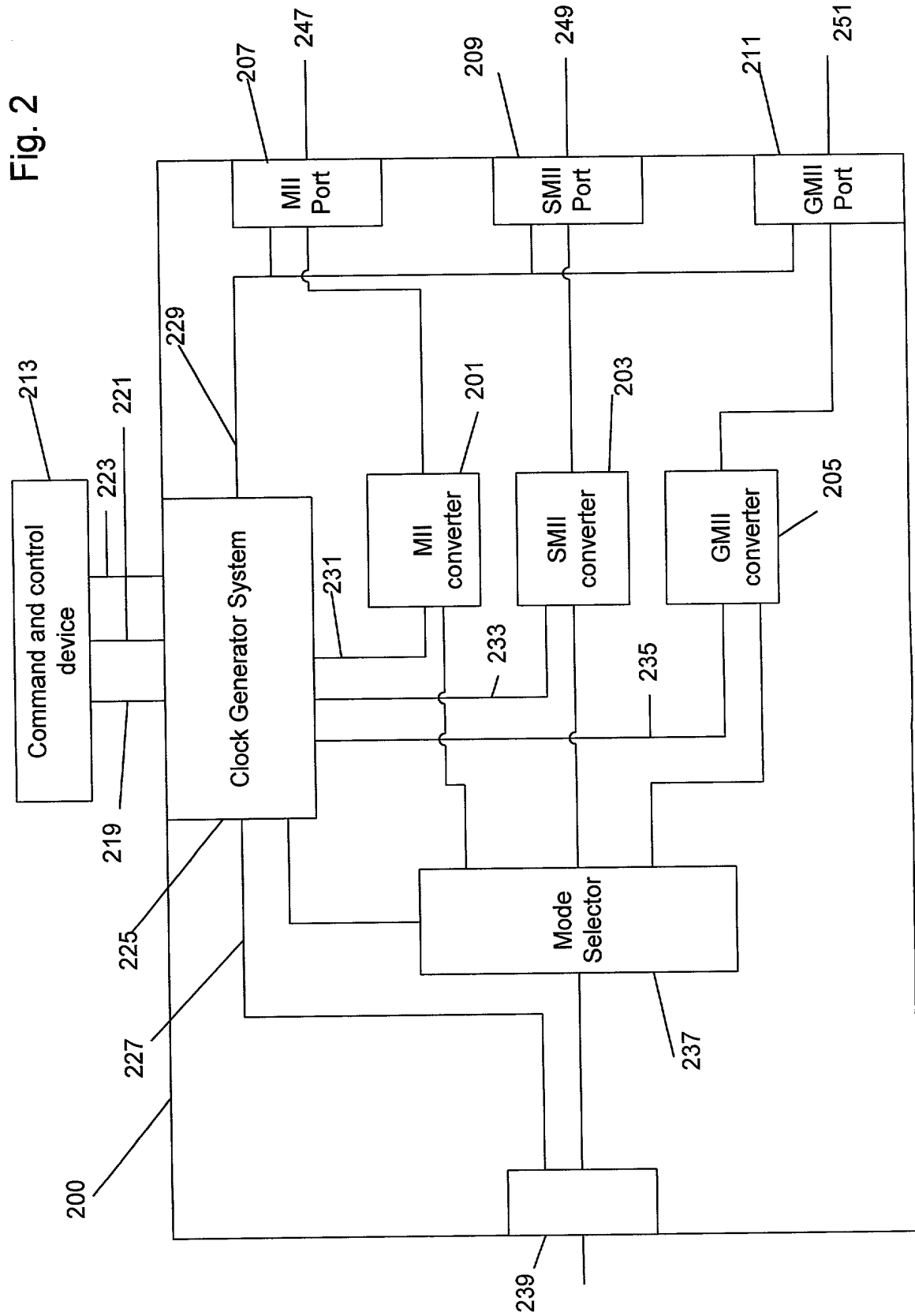


FIG. 3

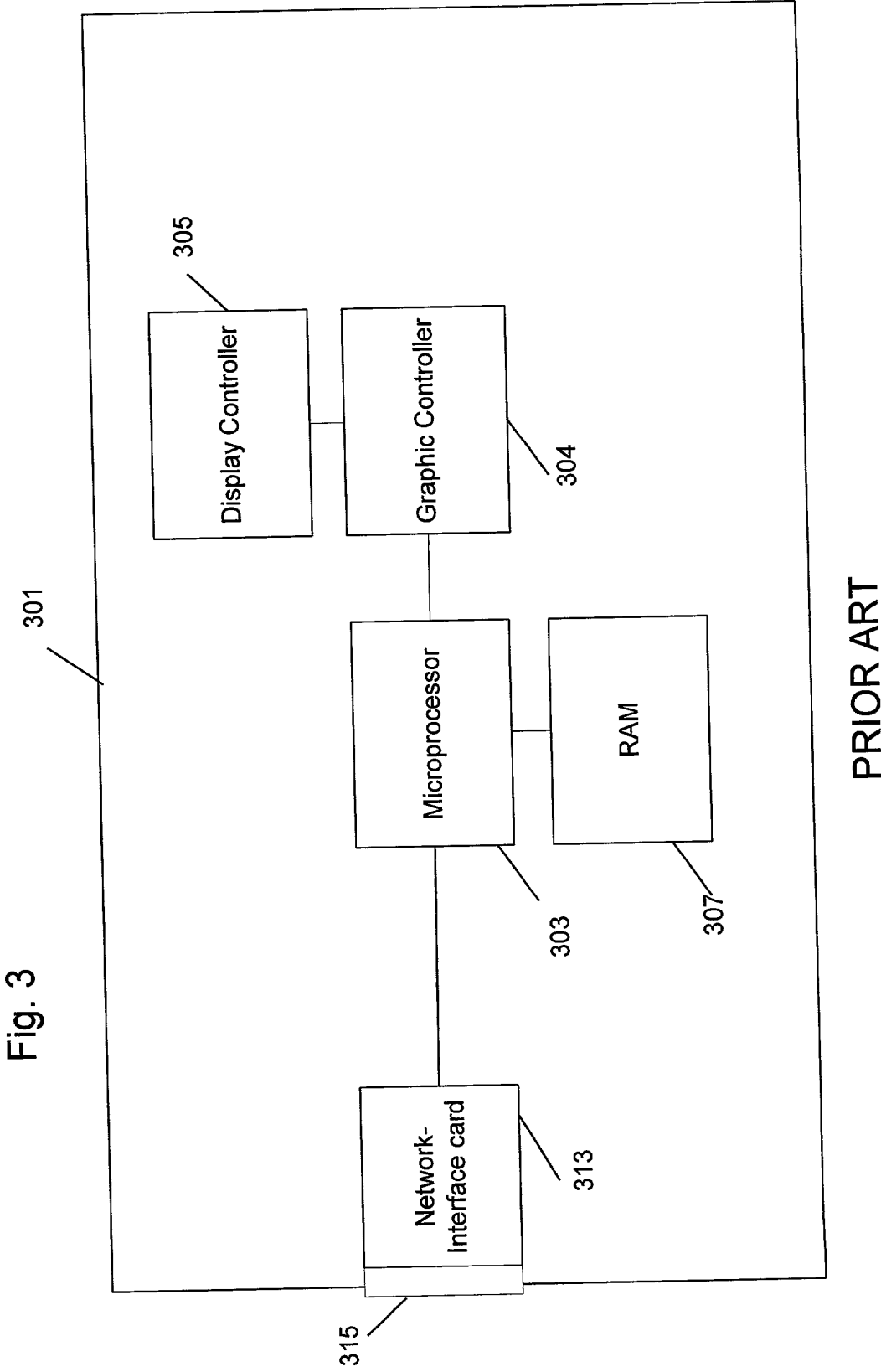


FIG. 4

Fig. 4

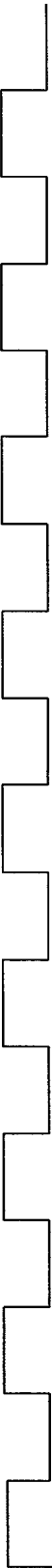
Master Clock, 10 MHz



System clock 1, 5 MHz



System clock 2, 5 MHz



MII clock, 5 MHz



Fig. 5

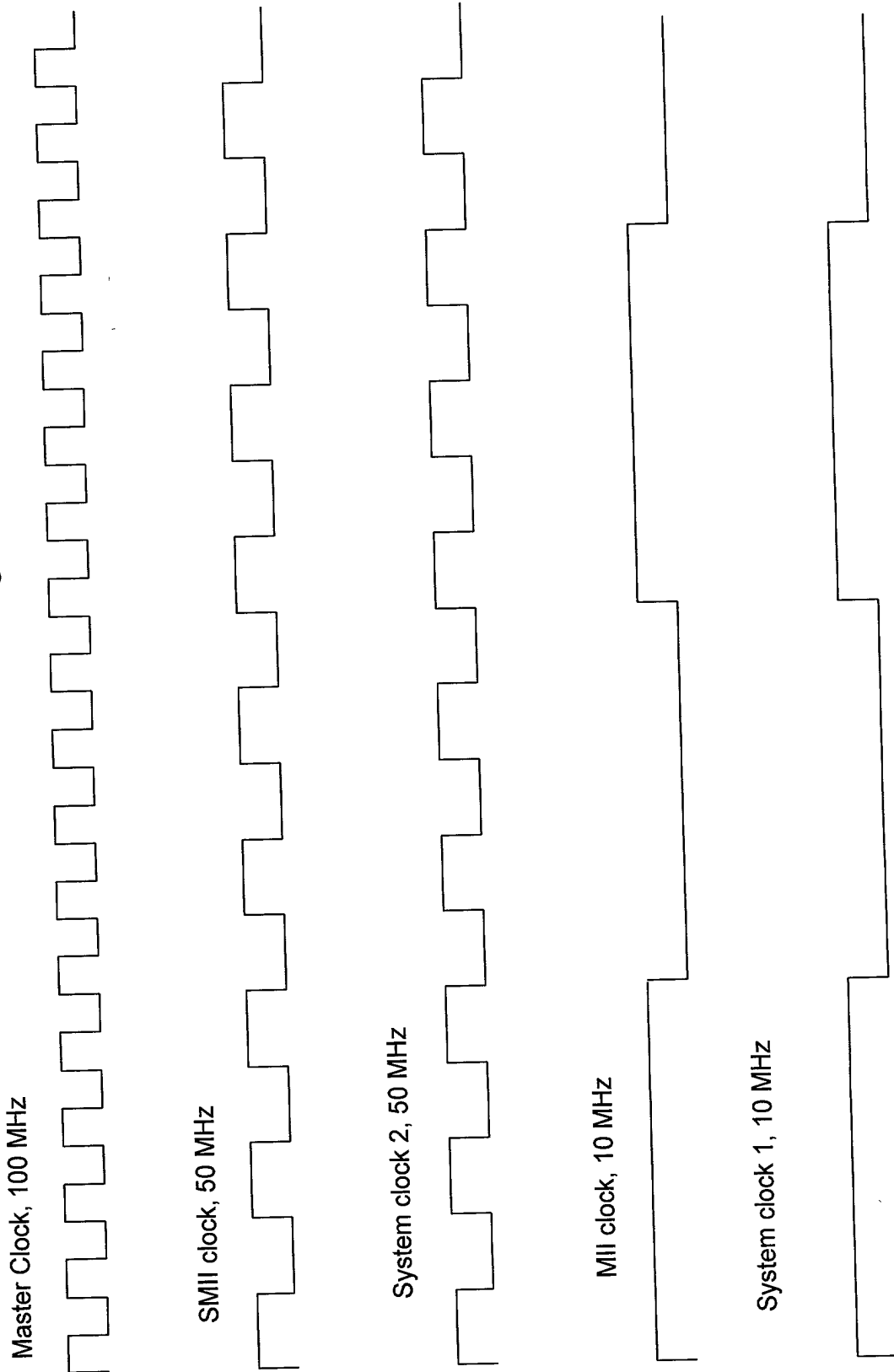


Fig. 6

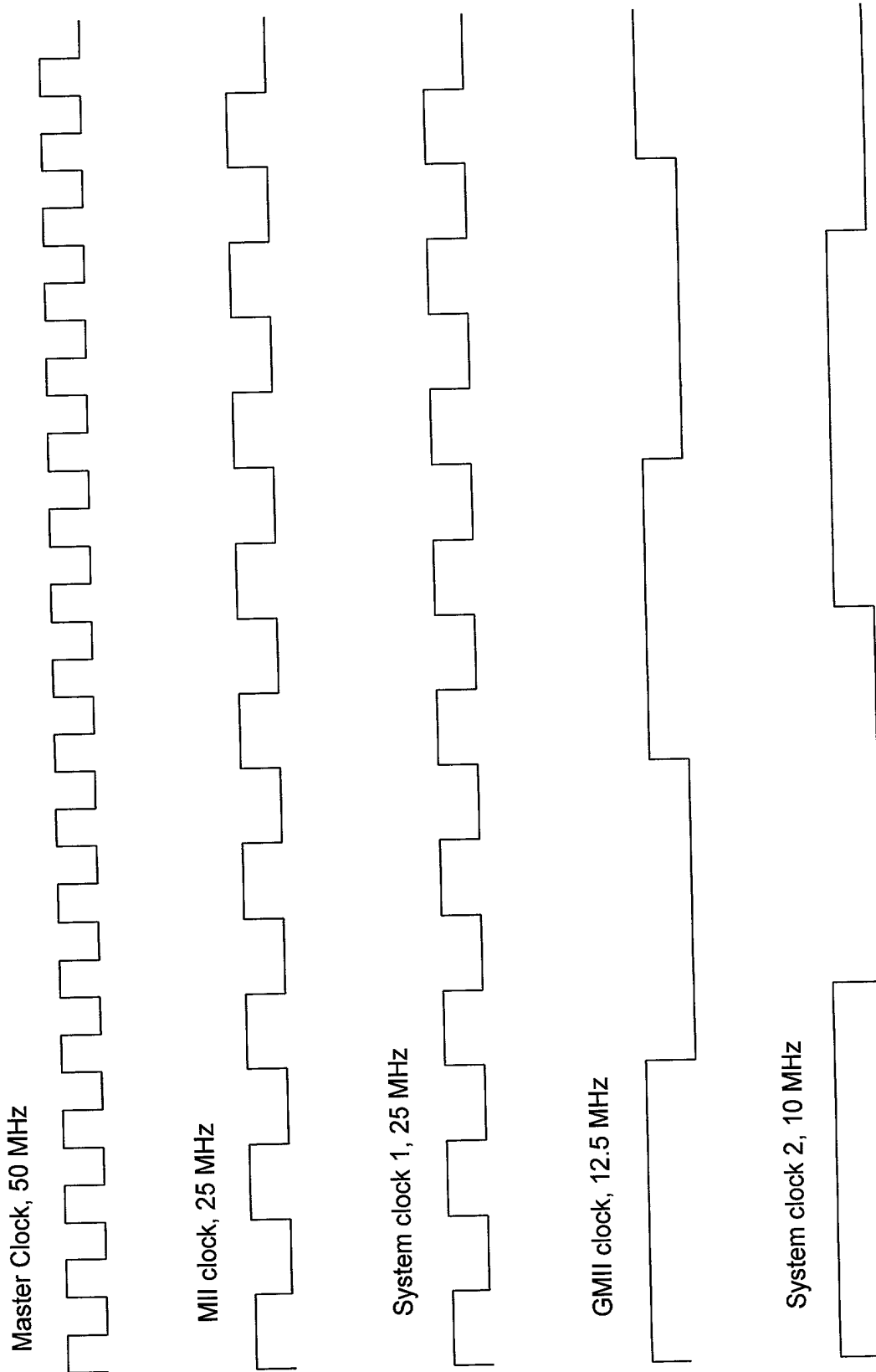


Fig. 7

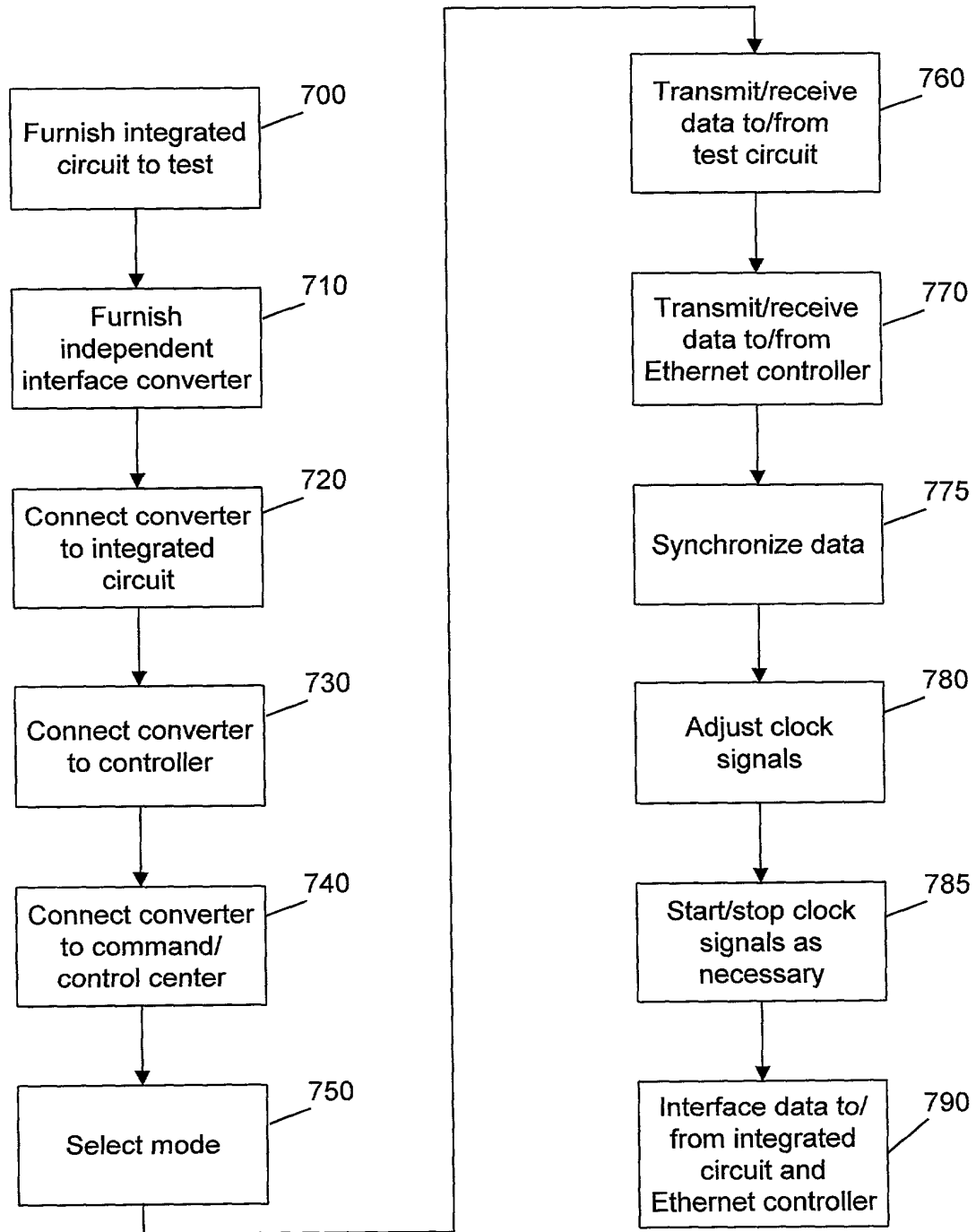


Fig. 8

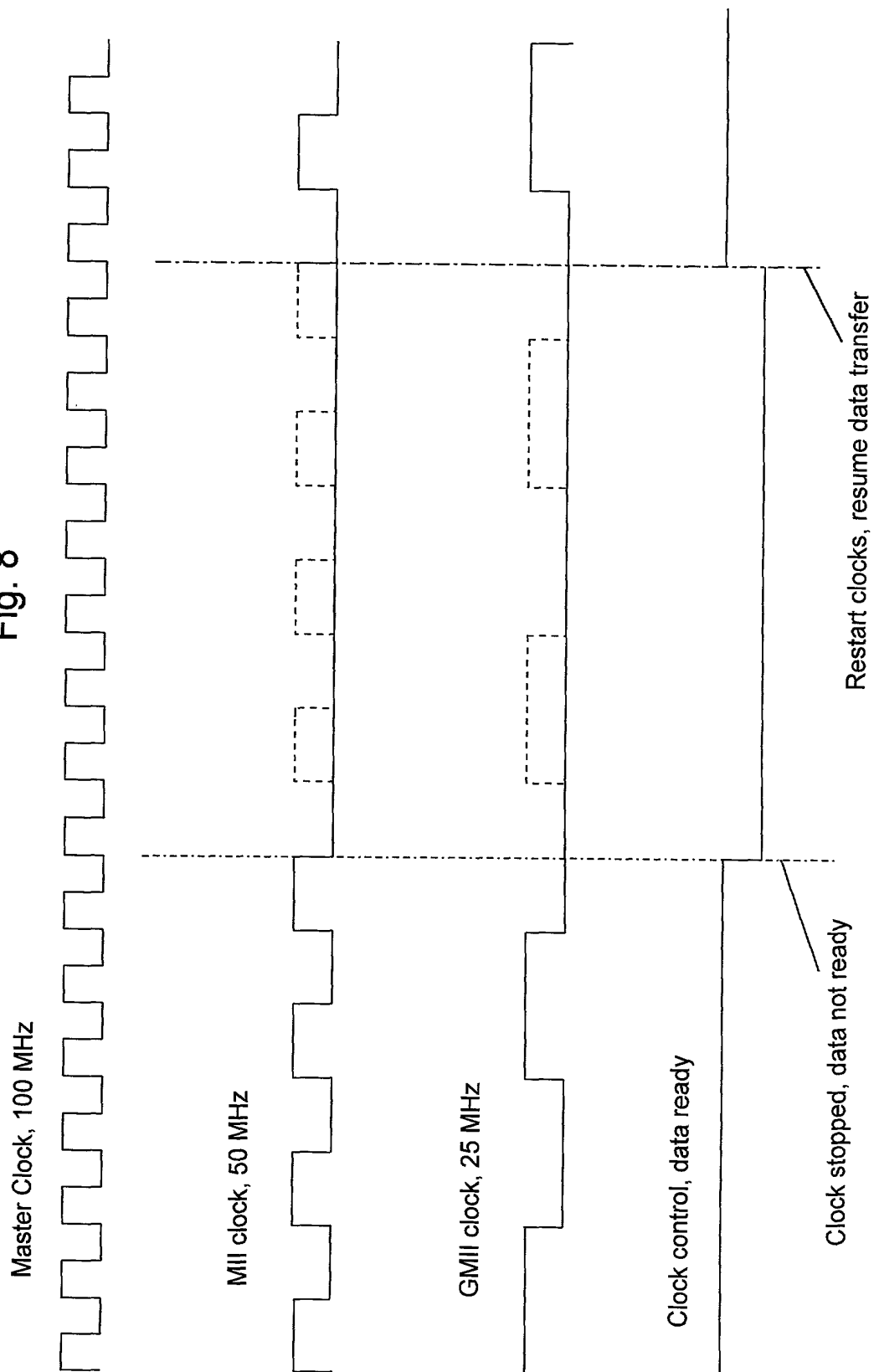




Fig. 9

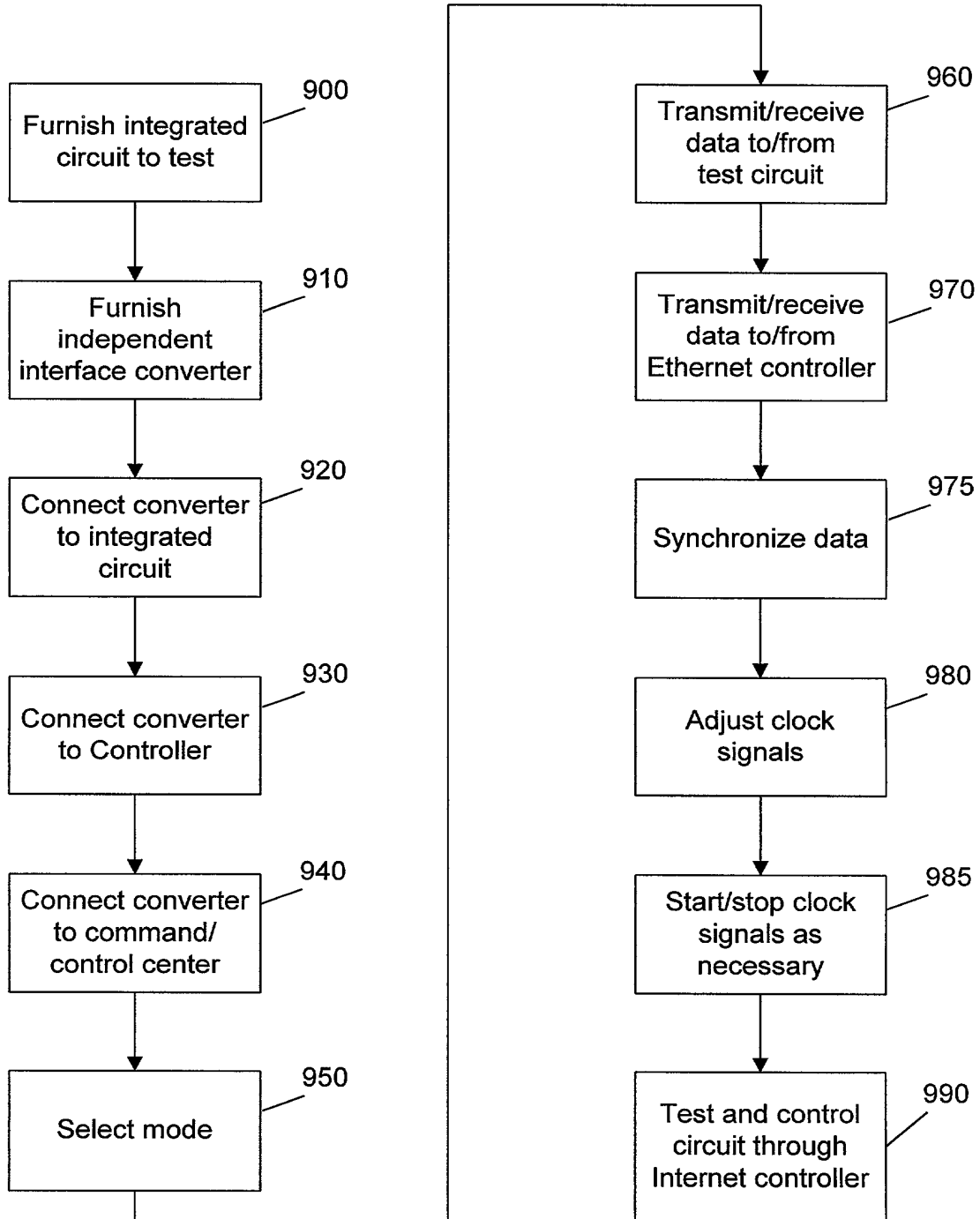


Fig. 10

